

ABSTRACT OF THE DISCLOSURE

Several sense amplifiers detect data read from
a memory cell of a bank in accordance with read
address. Several first holding circuits individually
5 hold data output from the sense amplifiers. Several
second holding circuits hold data output from the
corresponding first holding circuit after being delayed
by time that the read address gains with respect to the
burst address. Decoders individually supply data held
10 in several second holding circuits to the corresponding
line of a bus line.